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KEY=DOUGLAS - MARISA KARLEE

Basic Vlsi Design 3Rd Ed.

Basic VLSI Design

Principles and Applications

Prentice Hall

Basic VLSI Design

Basic VLSI Design

Systems and Circuits

Essentials Of Vlsi Circuits And Systems

PHI Learning Pvt. Ltd.

Study Guide to Basic VLSI Design

Fundamentals of Digital Logic Design, with VLSI Applications

Hardware -- Logic Design.

Basic VLSI Design

Principles and Applications

Prentice Hall

VLSI Design

I. K. International Pvt Ltd Aimed primarily for undergraduate students pursuing courses in VLSI design, the book emphasizes the physical understanding of underlying principles of the subject. It not only focuses on circuit design process obeying VLSI rules but also on technological aspects of Fabrication. VHDL modeling is discussed as the design engineer is expected to have good knowledge of it. Various Modeling issues of VLSI devices are focused which includes necessary device physics to the required level. With such an in-depth coverage and practical approach practising engineers can also use this as ready reference.

Vlsi Cad

PHI Learning Pvt. Ltd.

Multiprocessor Computer Architectures

North Holland For over forty years, most of the world has designed its computers after a single fashion. This style, which has come to be known as the von Neumann model after one of its originators, is showing increasing signs of being inadequately powerful for future needs. Efforts to discover alternative models, for so long a trickle, are at least becoming a flood. This book reports on the current status of a number of these efforts. This book is addressed to two main audiences. First, it is aimed at the reader who is new to the world of non-von Neumann computers, either in the area of design or that of application. By taking the form of a catalogue of projects bound in a single volume, it serves as a particularly convenient introductory text. Thus, it can be used to obtain a feel for the problems which designers are presently confronting and the solutions which they are developing. Indeed, the newcomer to the field, with the benefit of a fresh and unbiased viewpoint, may be in the best position to read through these collected reports and to identify some common point which has hitherto gone unnoticed or unexploited. Second, the book is aimed at the reader who is experienced in one particular area or discipline of non-von Neumann computer design. It serves as an introduction to hardware design for the computer scientist, and as an introduction to parallel programming philosophy for the electronics engineer.

Digital Vlsi Design

PHI Learning Pvt. Ltd.

ELECTRICAL AND ELECTRONIC MEASUREMENTS

PHI Learning Pvt. Ltd. In this modern scientific world a thorough understanding of complex measurements and instruments is the need of the hour. This book provides a comprehensive coverage of the concepts and principles of measurements and instrumentation, and brings into focus the recent and significant developments in this field. The book presents an exhaustive exposition of different types of measuring instruments and their applications in an easy-to-grasp manner. It presents even the minute details of various measurement techniques and calibration methods, which are the essential features of a measurement programme. The book elaborates on the theoretical background and practical knowledge of different measuring instruments to make the students accustomed to these devices. An in-depth coverage of topics makes the text useful to somewhat more advanced courses and its elaborated methodology will help students meet the challenges in their career. This book is ideally suitable for undergraduate students (BE/B.Tech.) of Electrical, Electronics and Instrumentation and Control disciplines of engineering. It can be also used as reference book for the cable testing, testing of instruments transformers, testing of energy meters and measurement of physical variables. KEY FEATURES : Gives a number of chapter-end review questions and numerical problems for practice. Includes plenty of diagrams to clarify the concepts. Contains about 250 problems and 200 solved examples for the benefit of the students.

A Practical Approach to VLSI System on Chip (SoC) Design

A Comprehensive Guide

Springer Nature This book provides a comprehensive overview of the VLSI design process. It covers end-to-end system on chip (SoC) design, including design methodology, the design environment, tools, choice of design components, handoff procedures, and design infrastructure needs. The book also offers critical guidance on the latest UPF-based low power design flow issues for deep submicron SOC designs, which will prepare readers for the challenges of working at the nanotechnology scale. This practical guide will provide engineers who aspire to be VLSI designers with the techniques and tools of the trade, and will also be a valuable professional reference for those already working in VLSI design and verification with a focus on complex SoC designs. A comprehensive practical guide for VLSI designers; Covers end-to-end VLSI SoC design flow; Includes source code, case studies, and application examples.

Analog and Mixed Mode Vlsi Design

PHI Learning Pvt. Ltd.

Modern VLSI Design

System-on-Chip Design

Pearson Education For Electrical Engineering and Computer Engineering courses that cover the design and technology of very large scale integrated (VLSI) circuits and systems. May also be used as a VLSI reference for professional VLSI design engineers, VLSI design managers, and VLSI CAD engineers. Modern VSLI Design provides a comprehensive "bottom-up" guide to the design of VSLI systems, from the physical design of circuits through system architecture with focus on the latest solution for system-on-chip (SOC) design. Because VSLI system designers face a variety of challenges that include high performance, interconnect delays, low power, low cost, and fast design turnaround time, successful designers must understand the entire design process. The Third Edition also provides a much more thorough discussion of hardware description languages, with introduction to both Verilog and VHDL. For that reason, this book presents the entire VSLI design process in a single volume.

CMOS VLSI Design: A Circuits and Systems Perspective

Pearson Education India

Modern VLSI Design

IP-based Design

Prentice-Hall PTR The award-winning VLSI design guide is now fully updated to reflect the latest advances in chip design

Broadband Access Networks

Introduction Strategies and Techno-economic Evaluation

Springer Science & Business Media The access network is expected to be one of the major battlegrounds of telecommunications network operators, since upgrades of the existing narrowband access network will be the critical factor in supplying multimedia broadband services in a competitive market. The future broadband access network architecture needs to be flexible enough to efficiently support the provision of a full set of broadband and narrowband services with a wide range of capacity demands. A wide range of broadband access technologies are available. Furthermore, the key issues in the upgrading of the very cost sensitive access network are financial as well as technological, both for incumbent and new entrant operators. Thus, in order to identify minimum-risk introductory strategies the economic viability of access network broadband upgrades needs to be carefully assessed. However, despite the definite need for techno-economic evaluations, very few books have been published in this field. One of the reasons might be that broadband access network upgrading only very recently gained wide recognition as a key challenge for broadband delivery. Secondly, this kind of strategic work and these studies tend to be considered rather sensitive by operators, and thus both results and methodologies are not usually readily available. Thirdly, the work reported in this book in many respects was a major pioneering effort, which quite ambitiously aimed at modelling the whole life-cycle costs and revenue streams of access network upgrades, as opposed to several other efforts, which often are limited to pure investment cost comparisons.

Introduction to VLSI Circuits and Systems

John Wiley & Sons Incorporated CD-ROM contains: AIM SPICE (from AIM Software) -- Micro-Cap 6 (from Spectrum Software) -- Silos III Verilog Simulator (from Simucad) -- Adobe Acrobat Reader 4.0 (from Adobe).

Static Timing Analysis for Nanometer Designs

A Practical Approach

Springer Science & Business Media iming, timing, timing! That is the main concern of a digital designer charged with designing a semiconductor chip. What is it, how is it T described, and how does one verify it? The design team of a large digital design may spend months architecting and iterating the design to achieve the required timing target. Besides functional verification, the t- ing closure is the major milestone which dictates when a chip can be - leased to the semiconductor foundry for fabrication. This book addresses the timing verification using static timing analysis for nanometer designs. The book has originated from many years of our working in the area of timing verification for complex nanometer designs. We have come across many design engineers trying to learn the background and various aspects of static timing analysis. Unfortunately, there is no book currently ava- able that can be used by a working engineer to get acquainted with the - tails of static timing analysis. The chip designers lack a central reference for information on timing, that covers the basics to the advanced timing veri- cation procedures and techniques.

VLSI Design

The second edition of VLSI Design is a comprehensive textbook designed for undergraduate students of electrical, electronics, and electronics and communication engineering. It provides a thorough understanding of the fundamental concepts and design of VLSI systems.

CMOS

Circuit Design, Layout, and Simulation

John Wiley & Sons Praise for CMOS: Circuit Design, Layout, and SimulationRevised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references,

amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning

Practical Low Power Digital VLSI Design

Springer Science & Business Media Practical Low Power Digital VLSI Design emphasizes the optimization and trade-off techniques that involve power dissipation, in the hope that the readers are better prepared the next time they are presented with a low power design problem. The book highlights the basic principles, methodologies and techniques that are common to most CMOS digital designs. The advantages and disadvantages of a particular low power technique are discussed. Besides the classical area-performance trade-off, the impact to design cycle time, complexity, risk, testability and reusability are discussed. The wide impacts to all aspects of design are what make low power problems challenging and interesting. Heavy emphasis is given to top-down structured design style, with occasional coverage in the semicustom design methodology. The examples and design techniques cited have been known to be applied to production scale designs or laboratory settings. The goal of Practical Low Power Digital VLSI Design is to permit the readers to practice the low power techniques using current generation design style and process technology. Practical Low Power Digital VLSI Design considers a wide range of design abstraction levels spanning circuit, logic, architecture and system. Substantial basic knowledge is provided for qualitative and quantitative analysis at the different design abstraction levels. Low power techniques are presented at the circuit, logic, architecture and system levels. Special techniques that are specific to some key areas of digital chip design are discussed as well as some of the low power techniques that are just appearing on the horizon. Practical Low Power Digital VLSI Design will be of benefit to VLSI design engineers and students who have a fundamental knowledge of CMOS digital design.

DIGITAL ELECTRONICS AND LOGIC DESIGN

PHI Learning Pvt. Ltd. Designed as a textbook for undergraduate students in Electrical Engineering, Electronics, Computer Science, and Information Technology, this up-to-date, well-organized study gives an exhaustive treatment of the basic principles of Digital Electronics and Logic Design. It aims at bridging the gap between these two subjects. The many years of teaching undergraduate and postgraduate students of engineering that Professor Somanathan Nair has done is reflected in the in-depth analysis and student-friendly approach of this book. Concepts are illustrated with the help of a large number of diagrams so that students can comprehend the subject with ease. Worked-out examples within the text illustrate the concepts discussed, and questions at the end of each chapter drill the students in self-study.

Digital Integrated Circuit Design

From VLSI Architectures to CMOS Fabrication

Cambridge University Press Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.

Proceedings of 2nd International Conference on Micro-Electronics, Electromagnetics and Telecommunications

ICMEET 2016

Springer The book is a collection of best papers presented in the Second International Conference on Microelectronics Electromagnetics and Telecommunication (ICMEET 2016), an international colloquium, which aims to bring together academic scientists, researchers and research scholars to discuss the recent developments and future trends in the fields of microelectronics, electromagnetics and telecommunication. Microelectronics research investigates semiconductor materials and device physics for developing electronic devices and integrated circuits with data/energy efficient performance in terms of speed, power consumption, and functionality. The book discusses various topics like analog, digital and mixed signal circuits, bio-medical circuits and systems, RF circuit design, microwave and millimeter wave circuits, green circuits and systems, analog and digital signal processing, nano electronics and giga scale systems, VLSI circuits and systems, SoC and NoC, MEMS and NEMS, VLSI digital signal processing, wireless communications, cognitive radio, and data communication.

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Skew-tolerant Circuit Design

Morgan Kaufmann As advances in technology and circuit design boost operating frequencies of microprocessors, DSPs and other fast chips, new design challenges continue to emerge. One of the major performance limitations in today's chip designs is clock skew, the uncertainty in arrival times between a pair of clocks. Increasing clock frequencies are forcing many engineers to rethink their timing budgets and to use skew-tolerant circuit techniques for both domino and static circuits. While senior designers have long developed their own techniques for reducing the sequencing overhead of domino circuits, this knowledge has routinely been protected as trade secret and has rarely been shared. Skew-Tolerant Circuit Design presents a systematic way of achieving the same goal and puts it in the hands of all designers. This book clearly presents skew-tolerant techniques and shows how they address the challenges of clocking, latching, and clock skew. It provides the practicing circuit designer with a clearly detailed tutorial and an insightful summary of the most recent literature on these critical clock skew issues. * Synthesizes the most recent advances in skew-tolerant design in one cohesive tutorial * Provides incisive instruction and advice punctuated by humorous illustrations * Includes exercises to test understanding of key concepts and solutions to selected exercises

BiCMOS Technology and Applications

Springer Science & Business Media The topic of bipolar compatible CMOS (BiCMOS) is a fascinating one and of ever-growing practical importance. The "technology pendulum" has swung from the two extremes of preeminence of bipolar in the 1950s and 60s to the apparent endless horizons for VLSI NMOS technology during the 1970s and 80s. Yet starting in the 1980s severallimits were clouding the horizon for pure NMOS technology. CMOS reemerged as a· viable high density, high performance technology. Similarly by the mid 1980s scaled bipolar devices had not only demonstrated new high speed records, but early versions of mixed bipolar/CMOS technology were being produced. Hence the paradigm of either high density . Q[high speed was metamorphasizing into an opportunity for both speed and density via a BiCMOS approach. Now as we approach the 1990s there have been a number of practical demonstrations of BiCMOS both for memory and logic applications and I expect the trend to escalate over the next decade. This book makes a timely contribution to the field of BiCMOS technology and circuit development. The evolution is now indeed rapid so that it is difficult to make such a book exhaustive of current developments. Probably equally difficult is the fact that the new technology opens a range of novel circuit opportunities that are as yet only formative in their development. Given these obstacles it is a herculean task to try to assemble a book on BiCMOS.

SOLID STATE DEVICES

PHI Learning Pvt. Ltd. Designed as a text for undergraduate students of engineering in Electrical, Electronics, and Computer Science and IT disciplines as well as undergraduate students (B.Sc.) of physics and electronics as also for postgraduate students of physics and electronics, this compact and accessible text endeavours to simplify the theory of solid state devices so that even an average student will be able to understand the concepts with ease. The authors, Prof. Somanathan Nair and Prof. S.R. Deepa, with their rich and long experience in teaching the subject, provide a detailed discussion of such topics as crystal structures of semiconductor materials, Miller indices, energy band theory of solids, energy level diagrams and mass action law. Besides, they give a masterly analysis of topics such as direct and indirect gap materials, Fermi–Dirac statistics, electrons in semiconductors, Hall effect, PN junction diodes, Zener and avalanche breakdowns, Schottky barrier diodes, bipolar junction transistors, MOS field-effect transistors, Early effect, Shockley diodes, SCRs, TRIAC, and IGBTs. In the Second Edition, two new chapters on opto-electronic devices and electro-optic devices have been added. The text has been thoroughly revised and updated. A number of solved problems and objective type questions have been included to help students develop grasp of the contents. This fully illustrated and well-organized text should prove invaluable to students pursuing various courses in engineering and physics. DISTINGUISHING FEATURES • Discusses the concepts in an easy-to-understand style. • Furnishes over 300 clear-cut diagrams to illustrate the discussed. • Gives a very large number of questions—short answer, fill in the blanks, tick the correct answer and review questions—to sharpen the minds of the reader. • Provides more than 200 fully solved numerical problems. • Gives answers to a large number of exercises.

Fundamentals of Digital Logic Design

With VLSI Circuit Applications

Digital VLSI Systems Design

A Design Manual for Implementation of Projects on

FPGAs and ASICs Using Verilog

Springer Science & Business Media This book provides step-by-step guidance on how to design VLSI systems using Verilog. It shows the way to design systems that are device, vendor and technology independent. Coverage presents new material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards. The reader is taken step by step through different designs, from implementing a single digital gate to a massive design consuming well over 100,000 gates. All the design codes developed in this book are Register Transfer Level (RTL) compliant and can be readily used or amended to suit new projects.

Earthquake Resistant Design and Risk Reduction

John Wiley & Sons Earthquake Resistant Design and Risk Reduction, 2nd edition is based upon global research and development work over the last 50 years or more, and follows the author's series of three books Earthquake Resistant Design, 1st and 2nd editions (1977 and 1987), and Earthquake Risk Reduction (2003). Many advances have been made since the 2003 edition of Earthquake Risk Reduction, and there is every sign that this rate of progress will continue apace in the years to come. Compiled from the author's wide design and research experience in earthquake engineering and engineering seismology, this key text provides an excellent treatment of the complex multidisciplinary process of earthquake resistant design and risk reduction. New topics include the creation of low-damage structures and the spatial distribution of ground shaking near large fault ruptures. Sections on guidance for developing countries, response of buildings to differential settlement in liquefaction, performance-based and displacement-based design and the architectural aspects of earthquake resistant design are heavily revised. This book: Outlines individual national weaknesses that contribute to earthquake risk to people and property Calculates the seismic response of soils and structures, using the structural continuum "Subsoil – Substructure – Superstructure – Non–structure" Evaluates the effectiveness of given design and construction procedures for reducing casualties and financial losses Provides guidance on the key issue of choice of structural form Presents earthquake resistant design methods for the main four structural materials – steel, concrete, reinforced masonry and timber – as well as for services equipment, plant and non-structural architectural components Contains a chapter devoted to problems involved in improving (retrofitting) the existing built environment This book is an invaluable reference and guiding tool to practising civil and structural engineers and architects, researchers and postgraduate students in earthquake engineering and engineering seismology, local governments and risk management officials.

An Introduction to VLSI System Design

Course Notes

Best Practices for Graphic Designers, Grids and Page Layouts

An Essential Guide for Understanding and Applying Page Design Principles

Rockport Pub This unique, go-to guide for designers fully details the essential layout and design skills needed to succeed in this competitive industry. With fun and practical application, it offers valuable insight into strategy and business when working in the real world with real clients, starting with basic information on layout principles before delving more deeply into theory and application on a project-by-project basis. Illustrated with real-world assignments and case studies, this guide offers a behind-the-scenes take on the entire process and steps necessary to go from concept to final outcome, including how to overcome challenges presented along the way.

VLSI Design

BoD – Books on Demand This book provides some recent advances in design nanometer VLSI chips. The selected topics try to present some open problems and challenges with important topics ranging from design tools, new post-silicon devices, GPU-based parallel computing, emerging 3D integration, and antenna design. The book consists of two parts, with chapters such as: VLSI design for multi-sensor smart systems on a chip, Three-dimensional integrated circuits design for thousand-core processors, Parallel symbolic analysis of large analog circuits on GPU platforms, Algorithms for CAD tools VLSI design, A multilevel memetic algorithm for large SAT-encoded problems, etc.

Physical Design Essentials

An ASIC Design Implementation Perspective

Springer Science & Business Media Arranged in a format that follows the industry-common ASIC physical design flow, Physical Design Essentials begins with general concepts of an ASIC library, then examines floorplanning, placement, routing, verification, and finally, testing. Among the topics covered are Basic standard cell design, transistor-sizing, and layout styles; Linear, non-linear, and polynomial characterization; Physical design constraints and floorplanning styles; Algorithms used for placement; Clock Tree Synthesis; Parasitic extraction; Electronic Testing, and many more.

Digital Integrated Circuits

A Design Perspective

Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design automation on the digital design perspective.

VLSI Fabrication Principles

Silicon and Gallium Arsenide

Wiley-Interscience In some places, the order of presentation has been changed to fine-tune the book's effectiveness as a senior and graduate-level teaching text. Fabrication principles covered include those for such circuits as CMOS, BIPOLAR, BICMOS, FET, and more.