

Art and Science of Microelectronic Circuit Design

Springer Nature This book guides readers through the entire complex of interrelated theoretical and practical aspects of the end-to-end design and organization of production of silicon submicron integrated circuits. The discussion includes the theoretical foundations of the operation of field-effect- and bipolar transistors, the methods and peculiarities of the structural and schematic design, basic circuit-design and system-design engineering solutions for bipolar, CMOS, BiCMOS and TTL integrated circuits, standard design libraries, and typical design flows. Provides a detailed description of the physical mechanisms and processes taking place inside the basic elements of design libraries; Shows how to control processes based on CMOS and bipolar technologies, that obtain the necessary values of operational speed, power consumption, electrical and dynamic parameters, and noise immunity of a specific integrated circuit; Introduces a new logic design algorithm for CMOS integrated circuits with extremely low power consumption.

Analysis and Design of Autonomous Microwave Circuits

John Wiley & Sons Presents simulation techniques that substantially increase designers' control over the oscillation in autonomous circuits This book facilitates a sound understanding of the free-running oscillation mechanism, the start-up from the noise level, and the establishment of the steady-state oscillation. It deals with the operation principles and main characteristics of free-running and injection-locked oscillators, coupled oscillators, and parametric frequency dividers. Analysis and Design of Autonomous Microwave Circuits provides: An exploration of the main nonlinear-analysis methods, with emphasis on harmonic balance and envelope transient methods Techniques for the efficient simulation of the most common autonomous regimes A presentation and comparison of the main stability-analysis methods in the frequency domain A detailed examination of the instabilization mechanisms that delimit the operation bands of autonomous circuits Coverage of techniques used to eliminate common types of undesired behavior, such as spurious oscillations, hysteresis, and chaos A thorough presentation of the oscillator phase noise A comparison of the main methodologies of phase-noise analysis Techniques for autonomous circuit optimization, based on harmonic balance A consideration of different design objectives: presetting the oscillation frequency and output power, increasing efficiency, modifying the transient duration, and imposing operation bands Analysis and Design of Autonomous Microwave Circuits is a valuable resource for microwave designers, oscillator designers, and graduate students in RF microwave design.

Mosfet Modeling for Circuit Analysis and Design

Microwave Wireless Communications

From Transistor to System Level

Academic Press To design and develop fast and effective microwave wireless systems today involves addressing the three different 'levels': Device, circuit, and system. This book presents the links and interactions between the three different levels rather than providing just a comprehensive coverage of one specific level. With the aim of overcoming the sectional knowledge of microwave engineers, this will be the first book focused on explaining how the three different levels interact by taking the reader on a journey through the different levels going from the theoretical background to the practical applications. Explains the links and interactions between the three different design levels of wireless communication transmitters: device, circuit, and system Presents state-of-the-art, challenges, and future trends in the field of wireless communication systems Covers all aspects of both mature and cutting-edge technologies for semiconductor devices for wireless communication applications Many circuit designs outlining the limitations derived from the available transistor technologies and system requirements Explains how new microwave measurement techniques can represent an essential tool for microwave modellers and designers

Simulation Techniques and Solutions for Mixed-Signal Coupling in Integrated Circuits

Springer Science & Business Media The goal of putting 'systems on a chip' has been a difficult challenge that is only recently being met. Since the world is 'analog', putting systems on a chip requires putting analog interfaces on the same chip as digital processing functions. Since some processing functions are accomplished more efficiently in analog circuitry, chips with a large amount of analog and digital circuitry are being designed. Whether a small amount of analog circuitry is combined with varying amounts of digital circuitry or the other way around, the problem encountered in marrying analog and digital circuitry are the same but with different scope. Some of the most prevalent problems are chip/package capacitive and inductive coupling, ringing on the RLC tuned circuits that form the chip/package power supply rails and off-chip drivers and receivers, coupling between circuits through the chip substrate bulk, and radiated emissions from the chip/package interconnects. To aggravate the problems of designers who have to deal with the complexity of mixed-signal coupling there is a lack of verification techniques to simulate the problem. In addition to considering RLC models for the various chip/package/board level parasitics, mixed-signal circuit designers must also model coupling through the common substrate when simulating ICs to obtain an accurate estimate of coupled noise in their designs. Unfortunately, accurate simulation of substrate coupling has only recently begun to receive attention, and techniques for the same are not widely known. Simulation Techniques and Solutions for Mixed-Signal Coupling in Integrated Circuits addresses two major issues of the mixed-signal coupling problem -- how to simulate it and how to overcome it. It identifies some of the problems that will be encountered, gives examples of actual hardware experiences, offers simulation techniques, and suggests possible solutions. Readers of this book should come away with a clear directive to simulate their design for interactions prior to building the design, versus a 'build it and see' mentality.

Three-dimensional Integrated Circuit Design

Morgan Kaufmann With vastly increased complexity and functionality in the "nanometer era" (i.e. hundreds of millions of transistors on one chip), increasing the performance of integrated circuits has become a challenging task. Connecting effectively (interconnect design) all of these chip elements has become the greatest determining factor in overall performance. 3-D integrated circuit design may offer the best solutions in the near future. This is the first book on 3-D integrated circuit design, covering all of the technological and design aspects of this emerging design paradigm, while proposing effective solutions to specific challenging problems concerning the design of 3-D integrated circuits. A handy, comprehensive reference or a practical design guide, this book provides a sound foundation for the design of 3-D integrated circuits. * Demonstrates how to overcome "interconnect bottleneck" with 3-D integrated circuit design...leading edge design techniques offer solutions to problems (performance/power consumption/price) faced by all circuit designers * The FIRST book on 3-D integrated circuit design...provides up-to-date information that is otherwise difficult to find * Focuses on design issues key to the product development cycle...good design plays a major role in exploiting the implementation flexibilities offered in the 3-D * Provides broad coverage of 3-D integrated circuit design, including interconnect prediction models, thermal management techniques, and timing optimization...offers practical view of designing 3-D circuits

Introduction to Logic Design

CRC Press The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic, instead of electronics, level. It covers a range of topics, from number system theory to asynchronous logic design. A solution manual is available to instructors only. Requests must be made on official school stationery.

Molecular Electronics, Circuits, and Processing Platforms

CRC Press When microelectronic devices replaced vacuum tubes, it marked a revolution in electronics that opened the way to the computer age. We are on the verge of witnessing another equally profound shift. As molecular devices replace semiconductors, we will achieve new levels of performance, functionality and capability that will hugely impact electronics, as well as signal processing and computing. Molecular Electronics, Circuits, and Processing Platforms guides you confidently into this emerging field. Helping you to forge into the molecular frontier, this book examines the various concepts, methods and technologies used to approach and solve a wide variety of problems. The author works from new devices to systems and platforms. He also covers device-level physics, system-level design, analysis, and advanced fabrication technologies. Explore the latest and emerging molecular, biomolecular, and nanoscale processing platforms for building the next generation of circuits, memories and computations. By examining both solved and open issues, this book thoroughly develops the basic theory and shows you how to apply this knowledge toward new developments and practical hardware implementation. Don't fall behind. Let Molecular Electronics, Circuits, and Processing Platforms take you to the next level of electronics design and applications.

Symbolic Analysis in Analog Integrated Circuit Design

Springer Science & Business Media Symbolic Analysis in Analog Integrated Circuit Design provides an introduction to computer-aided circuit analysis and presents systematic methods for solving linear (i.e. small-signal) and nonlinear circuit problems, which are illustrated by concrete examples. Computer-aided symbolic circuit analysis is useful in analog integrated circuit design. Analytic expressions for the network transfer functions contain information that is not provided by a numerical simulation result. However, these expressions are generally extremely long and difficult to interpret; therefore, it is necessary to be able to approximate them guided by the magnitude of the individual circuit parameters. Engineering has been described as 'the art of making approximations'. The inclusion of symbolic analysis in analog circuit design reduces the implied risk of ambiguity during the approximation process. A systematic method based on the nullor concept is used to obtain the basic feedback transistor amplifier configurations. Approximate expressions for the locations of poles and zeros for linear networks are obtained using the extended pole-splitting technique. An unusual feature in Symbolic Analysis in Analog Integrated Circuit Design is the consistent use of the transadmittance element with finite (linear or nonlinear) or infinite (i.e. nullor) gain as the only requisite circuit element. The describing function method is used to obtain approximate symbolic expressions for the harmonic distortion generated by a soft or hard transconductance nonlinearity embedded in

an arbitrary linear network. The design and implementation of a program (i.e. CASCA) for symbolic analysis of time-continuous networks is described. The algorithms can also be used to solve other linear problems, e.g. the analysis of time-discrete switched-capacitor networks. Symbolic Analysis in Analog Integrated Circuit Design serves as an excellent resource for students and researchers as well as for industry designers who want to familiarize themselves with circuit analysis. This book may also be used for advanced courses on the subject.

Analog Circuits and Systems Optimization based on Evolutionary Computation Techniques

Springer The microelectronics market, with special emphasis to the production of complex mixed-signal systems-on-chip (SoC), is driven by three main dynamics, time-- market, productivity and managing complexity. Pushed by the progress in nanometer technology, the design teams are facing a curve of complexity that grows exponentially, thereby slowing down the productivity design rate. Analog design automation tools are not developing at the same pace of technology, once custom design, characterized by decisions taken at each step of the analog design flow, - lies most of the time on designer knowledge and expertise. Actually, the use of - sign management platforms, like the Cadences Virtuoso platform, with a set of - tegrated CAD tools and database facilities to deal with the design transformations from the system level to the physical implementation, can significantly speed-up the design process and enhance the productivity of analog/mixed-signal integrated circuit (IC) design teams. These design management platforms are a valuable help in analog IC design but they are still far behind the development stage of design automation tools already available for digital design. Therefore, the development of new CAD tools and design methodologies for analog and mixed-signal ICs is essential to increase the designer's productivity and reduce design productivitygap. The work presented in this book describes a new design automation approach to the problem of sizing analog ICs.

Space Microelectronics Volume 2: Integrated Circuit Design for Space Applications

Artech House This invaluable second volume of a two-volume set is filled with details about the integrated circuit design for space applications. Various considerations for the selection and application of electronic components for designing spacecraft are discussed. The basic constructions of submicron transistors and schottky diodes during the technological process of production are explored. This book provides details on the energy consumption minimization methods for microelectronic devices. Specific topics include: Features and physical mechanisms of the effect of space radiation on all the main classes of microcircuits, including peculiarities of radiation impact on submicron integrated circuits;Special design, technology, and schematic methods of increasing the resistance to various types of space radiation;Recommendations for choosing research equipment and methods for irradiating various samples;Microcircuit designers on the composition of test elements for the study of the effect of radiation;Microprocessors, circuit boards, logic microcircuits, digital, analog, digital-analog microcircuits manufactured in various technologies (bipolar, CMOS, BiCMOS, SOI);Problems involved with designing high speed microelectronic devices and systems based on SOS-and SOI-structures;System-on-chip and system-in-package and methods for rejection of silicon microcircuits with hidden defects during mass production.

CAD of Circuits and Integrated Systems

John Wiley & Sons

Radio-Frequency and Microwave Communication Circuits Analysis and Design

John Wiley & Sons The products that drive the wireless communication industry, such as cell phones and pagers, employ circuits that operate at radio and microwave frequencies. Following on from a highly successful first edition, the second edition provides readers with a detailed introduction to RF and microwave circuits. Throughout, examples from real-world devices and engineering problems are used to great effect to illustrate circuit concepts. * Takes a top-down approach, describing circuits in the overall context of communication systems. * Presents expanded coverage of waveguides and FT mixers. * Discusses new areas such as oscillators design and digital communication. *An Instructor's Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department.

On-Chip ESD Protection for Integrated Circuits

An IC Design Perspective

Springer Science & Business Media This comprehensive and insightful book discusses ESD protection circuit design problems from an IC designer's perspective. On-Chip ESD Protection for Integrated Circuits: An IC Design Perspective provides both fundamental and advanced materials needed by a circuit designer for designing ESD protection circuits, including: Testing models and standards adopted by U.S. Department of Defense, EIA/JEDEC, ESD Association, Automotive Electronics Council, International Electrotechnical Commission, etc. ESD failure analysis, protection devices, and protection of sub-circuits Whole-chip ESD protection and ESD-to-circuit interactions Advanced low-parasitic compact ESD protection structures for RF and mixed-signal IC's Mixed-mode ESD simulation-design methodologies for design prediction ESD-to-circuit interactions, and more! Many real world ESD protection circuit design examples are provided. The book can be used as a reference book for working IC designers and as a textbook for students in the IC design field.

Basic Engineering Circuit Analysis

John Wiley & Sons Basic Engineering Circuit Analysis has long been regarded as the most dependable textbook for computer and electrical engineering majors. In this new edition, Irwin and Nelms continue to develop the most complete set of pedagogical tools available and provide the highest level of support for students entering into this complex subject. Irwin and Nelms trademark student-centered learning design focuses on helping students complete the connection between theory and practice. Key concepts are explained clearly and illustrated by detailed, worked examples. These are then followed by Learning Assessments, which allow students to work similar problems and check their results against the answers provided.

Introduction to PSpice Manual for Electric Circuits

Using Orcad Release 9.2

The fourth edition of this work continues to provide a thorough perspective of the subject, communicated through a clear explanation of the concepts and techniques of electric circuits. This edition was developed with keen attention to the learning needs of students. It includes illustrations that have been redesigned for clarity, new problems and new worked examples. Margin notes in the text point out the option of integrating PSpice with the provided Introduction to PSpice; and an instructor's roadmap (for instructors only) serves to classify homework problems by approach. The author has also given greater attention to the importance of circuit memory in electrical engineering, and to the role of electronics in the electrical engineering curriculum.

Loose Leaf for Engineering Circuit Analysis

McGraw-Hill Education

Analog and Mixed-Signal Electronics

John Wiley & Sons A practical guide to analog and mixed-signal electronics, with an emphasis on design problems and applications This book provides an in-depth coverage of essential analog and mixed-signal topics such as power amplifiers, active filters, noise and dynamic range, analog-to-digital and digital-to-analog conversion techniques, phase-locked loops, and switching power supplies. Readers will learn the basics of linear systems, types of nonlinearities and their effects, op-amp circuits, the high-gain analog filter-amplifier, and signal generation. The author uses system design examples to motivate theoretical explanations and covers system-level topics not found in most textbooks. Provides references for further study and problems at the end of each chapter Includes an appendix describing test equipment useful for analog and mixed-signal work Examines the basics of linear systems, types of nonlinearities and their effects, op-amp circuits, the high-gain analog filter-amplifier, and signal generation Comprehensive and detailed, Analog and Mixed-Signal Electronics is a great introduction to analog and mixed-signal electronics for EE undergraduates, advanced electronics students, and for those involved in computer engineering, biomedical engineering, computer science, and physics.

Basic Engineering Circuit Analysis

John Wiley & Sons **Basic Engineering Circuit Analysis** has long been regarded as the most dependable textbook for computer and electrical engineering majors. In this new edition, Irwin and Nelms continue to develop the most complete set of pedagogical tools available and provide the highest level of support for students entering into this complex subject. Irwin and Nelms trademark student-centered learning design focuses on helping students complete the connection between theory and practice. Key concepts are explained clearly and illustrated by detailed, worked examples. These are then followed by Learning Assessments, which allow students to work similar problems and check their results against the answers provided.

Power System Analysis and Design

Cengage Learning **Examine the basic concepts behind today's power systems as well as the tools you need to apply your newly acquired skills to real-world situations with POWER SYSTEM ANALYSIS AND DESIGN, 7th Edition.** The latest updates throughout this new edition reflect the most recent trends in the field as the authors highlight key physical concepts with clear explanations of important mathematical techniques. New co-author Adam Birchfield joins this prominent author team with fresh insights into the latest technological advancements. The authors develop theory and modeling from simple beginnings, clearly demonstrating how you can apply the principles you learn to new, more complex situations. New learning objectives and helpful case study summaries help focus your learning and guide you in developing important provide design experience. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

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Computer Aided Design and Design Automation

CRC Press **This volume of The Circuits and Filters Handbook, Third Edition focuses on computer aided design and design automation.** In the first part of the book, international contributors address topics such as the modeling of circuit performances, symbolic analysis methods, numerical analysis methods, design by optimization, statistical design optimization, and physical design automation. In the second half of the text, they turn their attention to RF CAD, high performance simulation, formal verification, RTK behavioral synthesis, system-level design, an Internet-based micro-electronic design automation framework, performance modeling, and embedded computing systems design.

Skew-tolerant Circuit Design

Morgan Kaufmann **As advances in technology and circuit design boost operating frequencies of microprocessors, DSPs and other fast chips, new design challenges continue to emerge. One of the major performance limitations in today's chip designs is clock skew, the uncertainty in arrival times between a pair of clocks. Increasing clock frequencies are forcing many engineers to rethink their timing budgets and to use skew-tolerant circuit techniques for both domino and static circuits. While senior designers have long developed their own techniques for reducing the sequencing overhead of domino circuits, this knowledge has routinely been protected as trade secret and has rarely been shared. Skew-Tolerant Circuit Design presents a systematic way of achieving the same goal and puts it in the hands of all designers. This book clearly presents skew-tolerant techniques and shows how they address the challenges of clocking, latching, and clock skew. It provides the practicing circuit designer with a clearly detailed tutorial and an insightful summary of the most recent literature on these critical clock skew issues. * Synthesizes the most recent advances in skew-tolerant design in one cohesive tutorial * Provides incisive instruction and advice punctuated by humorous illustrations * Includes exercises to test understanding of key concepts and solutions to selected exercises**

Pathological Elements in Analog Circuit Design

Springer **This book is a compilation and a collection of tutorials and recent advances in the use of nullors (combinations of nullators and norators) and pathological mirrors in analog circuit and system design. It highlights the basic theory, trends and challenges in the field, making it an excellent reference resource for researchers and designers working in the synthesis, analysis, and design of analog integrated circuits. With its tutorial character, it can also be used for teaching. Singular elements such as nullors and pathological mirrors can arguably be considered as universal blocks since they can represent all existing analog building blocks, and they allow complex integrated circuits to be designed simply and effectively. These pathological elements are now used in a wide range of applications in modern circuit/system theory, and also in design practice.**

Analysis and Solutions for Switching Noise Coupling in Mixed-Signal ICs

Springer Science & Business Media **Modern microelectronic design is characterized by the integration of full systems on a single die. These systems often include large high performance digital circuitry, high resolution analog parts, high driving I/O, and maybe RF sections. Designers of such systems are constantly faced with the challenge to achieve compatibility in electrical characteristics of every section: some circuitry presents fast transients and large consumption spikes, whereas others require quiet environments to achieve resolutions well beyond millivolts. Coupling between those sections is usually unavoidable, since the entire system shares the same silicon substrate bulk and the same package. Understanding the way coupling is produced, and knowing methods to isolate coupled circuitry, and how to apply every method, is then mandatory knowledge for every IC designer. Analysis and Solutions for Switching Noise Coupling in Mixed-Signal ICs is an in-depth look at coupling through the common silicon substrate, and noise at the power supply lines. It explains the elementary knowledge needed to understand these phenomena and presents a review of previous works and new research results. The aim is to provide an understanding of the reasons for these particular ways of coupling, review and suggest solutions to noise coupling, and provide criteria to apply noise reduction. Analysis and Solutions for Switching Noise Coupling in Mixed-Signal ICs is an ideal book, both as introductory material to noise-coupling problems in mixed-signal ICs, and for more advanced designers facing this problem.**

Linear Circuit Analysis

Time Domain, Phasor, and Laplace Transform Approaches

The combined three volumes of these texts cover traditional linear circuit analysis topics - both concepts and computation - including the use of available software for problem solution where necessary. The text balances emphasis on concepts and calculation so students learn the basic principles and properties that govern circuits behaviour, while they gain a firm understanding of how to solve computational techniques they will face in the world of professional engineers.

Electric Circuits

Cengage Learning **Now readers can master the fundamentals of electric circuits with Kang's ELECTRIC CIRCUITS. Readers learn the basics of electric circuits with common design practices and simulations as the book presents clear step-by-step examples, practical exercises, and problems. Each chapter includes several examples and problems related to circuit design, with answers for odd-numbered questions so learners can further prepare themselves with self-guided study and practice. ELECTRIC CIRCUITS covers everything from DC circuits and AC circuits to Laplace transformed circuits. MATLAB scripts for certain examples give readers an alternate method to solve circuit problems, check answers, and reduce laborious derivations and calculations. This edition also provides PSpice and Simulink examples to demonstrate electric circuit simulations. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.**

System Engineering Analysis, Design, and Development

Concepts, Principles, and Practices

John Wiley & Sons **Praise for the first edition: "This excellent text will be useful to every system engineer (SE) regardless of the domain. It covers ALL relevant SE material and does so in a very clear, methodical fashion. The breadth and depth of the author's presentation of SE principles and practices is outstanding." -Philip Allen** This textbook presents a comprehensive, step-by-step guide to System Engineering analysis, design, and development via an integrated set of concepts, principles, practices, and methodologies. The methods presented in this text apply to any type of human system -- small, medium, and large organizational systems and system development projects delivering engineered systems or services across multiple business sectors such as medical, transportation, financial, educational, governmental, aerospace and defense, utilities, political, and charity, among

others. Provides a common focal point for “bridging the gap” between and unifying System Users, System Acquirers, multi-discipline System Engineering, and Project, Functional, and Executive Management education, knowledge, and decision-making for developing systems, products, or services. Each chapter provides definitions of key terms, guiding principles, examples, author’s notes, real-world examples, and exercises, which highlight and reinforce key SE&D concepts and practices. Addresses concepts employed in Model-Based Systems Engineering (MBSE), Model-Driven Design (MDD), Unified Modeling Language (UML) / Systems Modeling Language (SysML), and Agile/Spiral/V-Model Development such as user needs, stories, and use cases analysis; specification development; system architecture development; User-Centric System Design (UCSD); interface definition & control; system integration & test; and Verification & Validation (V&V). Highlights/introduces a new 21st Century Systems Engineering & Development (SE&D) paradigm that is easy to understand and implement. Provides practices that are critical staging points for technical decision making such as Technical Strategy Development; Life Cycle requirements; Phases, Modes, & States; SE Process; Requirements Derivation; System Architecture Development, User-Centric System Design (UCSD); Engineering Standards, Coordinate Systems, and Conventions; et al. Thoroughly illustrated, with end-of-chapter exercises and numerous case studies and examples, Systems Engineering Analysis, Design, and Development, Second Edition is a primary textbook for multi-discipline, engineering, system analysis, and project management undergraduate/graduate level students and a valuable reference for professionals.

Microelectronic Circuits: Analysis and Design

Cengage Learning **MICROELECTRONIC CIRCUITS: ANALYSIS AND DESIGN, 3E** combines a breadth-first approach to learning electronics with a strong emphasis on design and simulation. This book first introduces the general characteristics of circuits (ICs) in preparation for using circuit design and analysis techniques. This edition then offers a more detailed study of devices and circuits and how they operate within ICs. More than half of the problems and examples concentrate on design and emphasize how to use computer software tools extensively. The book’s proven sequence introduces electronic devices and circuits, then electronic circuits and applications, and finally, digital and analog integrated circuits. Readers learn to apply theory to real-world design problems as they master the skills to test and verify their designs. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

Low-Voltage Low-Power CMOS Current Conveyors

Springer Science & Business Media This concise and modern book on current conveyors considers first and second-generation devices in a general environment and for low-voltage low-power applications. It constitutes an excellent reference for analogue designers and researchers and is suitable as a textbook in an advanced course on microelectronics.

Yield-Aware Analog IC Design and Optimization in Nanometer-scale Technologies

Springer Nature This book presents a new methodology with reduced time impact to address the problem of analog integrated circuit (IC) yield estimation by means of Monte Carlo (MC) analysis, inside an optimization loop of a population-based algorithm. The low time impact on the overall optimization processes enables IC designers to perform yield optimization with the most accurate yield estimation method, MC simulations using foundry statistical device models considering local and global variations. The methodology described by the authors delivers on average a reduction of 89% in the total number of MC simulations, when compared to the exhaustive MC analysis over the full population. In addition to describing a newly developed yield estimation technique, the authors also provide detailed background on automatic analog IC sizing and optimization.